# VC-101MC-M/C 8 H VC-151MC-M/C 5 H

Ultra High Resolution CMOS Digital Camera



The VC-101MC and VC-151MC, the latest models of the industrial proven VC series, are 101 and 151 megapixel resolution CMOS cameras available with the Camera Link interface. These cameras are based on the latest CMOS image sensor technology (IMX461 and IMX411) from Sony Semiconductor Solutions Corporation. The VC-101MC-8 offers up to 8.1 frames per second at 11648  $\times$  8742 resolution. For even higher resolution applications, the VC-151MC-5 offers up to 5.5 frames per second at 14192  $\times$  10640 resolution. Equipped with the Vieworks' innovative technologies proved by world's top FPD manufacturers, the VC-101MC and VC-151MC cameras offer not only highly uniformed images but also high speed image processing capabilities. Featured with high quality image uniformity and high resolution, these cameras are ideal for demanding applications such as FPD, PCB and semiconductor inspections.



#### VC-101MC-8 H / VC-151MC-5 H

Ultra High Resolution CMOS Digital Camera

#### Main Features

- 101 or 151 Megapixel Resolution
- Camera Link Full Interface
- Electronic Rolling Shutter
- DSNU and PRNU Correction
- Flat Field Correction with Sequencer Control
- Hot Pixel Correction
- Dynamic Defective Pixel Correction

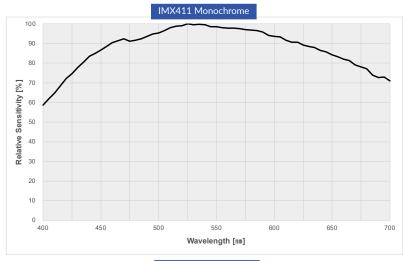
## **Applications**

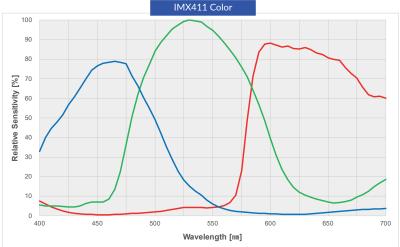
- Flat Panel Display Inspection
- Electronics Inspection
- Semiconductor Inspection
- Document / Film Scanning

## **Specifications**

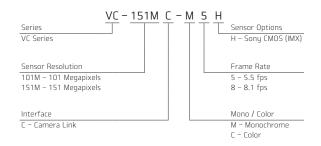
Model		VC-101MC-M/C 8 H	VC-151MC-M/C 5 H
Resolution (H $ imes$ V)		11648 × 8742	14192 × 10640
Sensor		SONY IMX461	SONY IMX411
Sensor Size (Diagonal)		43.80 mm $ imes$ 32.87 mm (55 mm)	53.36 mm $ imes$ 40.01 mm (66.7 mm)
Pixel Size		3.76 µm × 3.76 µm	
Interface		Camera Link Base / Medium / Full / 10 Tap, 26-pin SDR Connector	
Max. Frame Rate		8.1 fps (with Overlapped Acquisition)	5.5 fps (with Overlapped Acquisition)
Exposure Time (1 $\mu$ s step)		1 μs - 60 s	
Binning	Sensor	×1, ×3 (Horizontal and Vertical Dependent)	
	Logic	imes1, $ imes$ 2, $ imes$ 4 (Horizontal and Vertical Independent)	
Pixel Data Format		8 / 10 / 12 bit	
Data Output Pixel Clock Speed		65 MHz / 85 MHz	
Electronic Shutter		Rolling Shutter	
Trigger Synchronization	Overlapped Acquisition	Free-Run	
	Non-overlapped Acquisition	Hardware Trigger or CC1	
Dynamic Range		78 dB	
Gain Control		1× ~ 32×	
Black Level Control		0 ~ 255 LSB at 12 bit	
Dimension / Weight		90 mm $ imes$ 90 mm $ imes$ 92.5 mm, 800 g (with M-72 mount)	100 mm $\times$ 100 mm $\times$ 92.5 mm, 1070 g (with M-72 mount)
Temperature		Operating: 0°C ~ 40°C, Storage: −40°C ~ 70°C	
Lens Mount		M72-mount, Custom mount available upon request	
Power	External	11 ~ 24 V DC	
	Dissipation	Typ. 15.5 W	
Compliance		CE, FCC, KC	
API SDK		Vieworks Imaging Solution 7.X	

#### **Relative Sensitivity Curves**





## **Ordering Scheme**



## **Connector Specification**





1, 2, 3: +12V DC 4, 5, 6: GND (HR10A-7R-6PB)

Control



1: Trigger IN+ 2: Trigger IN-3: Strobe Out-(GND) 4: Strobe OUT+ (HR10A-7R-4S)

Connectors on camera body

#### **Mechanical Dimensions**

Unit: mm

